Working at Eta Wireless

Eta Wireless is solving the fundamental power challenges in the mobile communications industry. Today’s smartphones, wearables and Internet of Things (IoT) devices are highly energy inefficient and typically waste most of the power consumed as heat. Our game changing technology, ETAAdvanced, dramatically reduces energy consumption to significantly extend the battery life of all wireless communication devices. Unlike legacy power management technologies, ETAAdvanced supports new gigabit communication standards for LTE Advanced, 5G and WiFi. With ETAAdvanced, running out of power becomes a thing of the past.

Based on breakthrough research by two MIT professors, Eta Wireless is headquartered in Cambridge, MA, USA. The founders of Eta Wireless previously built Eta Devices, a company providing proven power management solutions for mobile base stations that was acquired by Nokia in October 2016.

Based on strong market traction, we are looking to expand our technical team with outstanding talent. At Eta Wireless, you get to work together with some of the smartest people in the industry to push the boundaries of power management for wireless communications. We thrive on solving problems that matter and making a lasting impact. We are not looking for people seeking comfortable 9-to-5 office jobs; we are looking for people who are passionate about changing the world – people who can imagine the future and who possess the talent and drive to create the future.

We are a young company with a culture that is dynamic, innovative, fast-paced and performance-oriented. We offer competitive packages to attract the best global talent.

For any career inquiry, please contact careers@etawireless.com
Job description: Principal Digital Design Engineer

As Principal Digital Design engineer, you will be responsible for all aspects of the digital system development for highly efficient wireless transmitter designs targeting 4G/WLAN radio standards from concept to production including specification, simulation, design, layout, verification, delivery (handoff or GDS), validation, and introduction into manufacturing.

Responsibilities for the position include digital architecture definition and design with emphasis on minimizing resource allocation and power dissipation. The nature of ETAdvanced technology places special focus on design of digital signal processing algorithms. The Principal Digital Design Engineer is responsible for digital aspects of the complete IC design, or in different scenarios RTL- or GDS-level handoff.

Experience in design specifically for RF signal processing subsystems or RF envelope tracking systems is highly desirable. You will collaborate with a team of experts in RF, digital, analog, software, and algorithms to optimize the complete solution.

Qualifications

• Background/requirements: Experienced BSEE/MSEE/PhD Engineer
• Track record of industry success (mobile device industry preferred)
• Knowledge in ASIC design and architectures with understanding of physical design
• SoC experience: Knowledge in embedded software system design (HW/SW integration)
• Timing management and static timing analysis
• Design verification: Signal integrity analysis, automated regression testing, gate level simulation, UVM, RTL-Gate equivalence checking
• RTL design for FPGA and ASIC targets
• Experience with C- or Matlab-based Verilog co-simulation
• Experience with full Cadence digital design tool chain flow; analog and AMS experience also desired
• Hands-on experience with testing and troubleshooting designs in the lab
• Good organizational skills: planning, documenting, follow-through, ability to manage details
• Excels in a fast-paced dynamic environment with strong influence on technical success and direction

Eta Wireless offers an exceptional compensation plan including equity and a competitive benefits package. Interested candidates are invited to send their resume and cover letter to careers@etawireless.com