Eta Wireless is looking for a
DSP-Focused RTL Verification Engineer

Working at Eta Wireless
Eta Wireless is solving the fundamental power challenges in the mobile communications industry. Today’s smartphones, wearables and Internet of Things (IoT) devices are highly energy inefficient and typically waste most of the power consumed as heat. Our game changing technology, ETAdvanced, dramatically reduces energy consumption to significantly extend the battery life of all wireless communication devices. Unlike legacy power management technologies, ETAdvanced supports new gigabit communication standards for LTE Advanced, 5G and WiFi. With ETAdvanced, running out of power becomes a thing of the past.

Based on breakthrough research by two MIT professors, Eta Wireless is headquartered in Cambridge, MA, USA. The founders of Eta Wireless previously built Eta Devices, a company providing proven power management solutions for mobile base stations that was acquired by Nokia in October 2016.

Based on strong market traction, we are looking to expand our technical team with outstanding talent. At Eta Wireless, you get to work together with some of the smartest people in the industry to push the boundaries of power management for wireless communications. We thrive on solving problems that matter and making a lasting impact. We are not looking for people seeking comfortable 9-to-5 office jobs; we are looking for people who are passionate about changing the world – people who can imagine the future and who possess the talent and drive to create the future.

We are a young company with a culture that is dynamic, innovative, fast-paced and performance-oriented. We offer competitive packages to attract the best global talent.

For any career inquiry, please contact careers@etawireless.com
Job description: DSP-Focused RTL Verification Engineer

Eta Wireless is looking for a verification engineer who has experience with hands-on development code for verification for DSP subsystems implemented in Verilog.

Your experience includes working in modern agile environments with UVM and System Verilog interfaces.

The verification engineer develops test-benches using System Verilog and integrates bit accurate models and designs regression suites. The work includes post process of test data using Python/Perl and Matlab. Develops test code using System Verilog assertions and cover-points.

Qualifications

• Background/requirements: Experienced BSEE/MSEE/PhD Engineer
• Verification and test of fixed point DSP systems, multi-rate systems, Interpolation, decimation using Matlab/C for stimulus and analysis.
• Hands-on experience in developing C test code, C functions specific to signal processing functionality.
• Development of C code which is bit accurate to HW/RTL. Tracking and analysis of Quantization noise.
• Model floating point vs fixed point errors.
• Hands-on experience with Z transforms, FFTS, SNR and spectral analysis.
• Hands-on experience testing fixed point DSP hardware.
• Exceptional organizational skills: planning, documenting, follow-through, ability to manage details.
• Ability to learn and explore new technology areas and applications in a start-up environment.